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implicitly includes auxiliary **tables**, e.g., a symbol **table**, a constant, or label **table** ... perform tree walks or use tree **pattern** matching algorithms A **DAG** is a contraction of the AST that avoids unnecessary ... Much of the improvement that **compilers** can make in **optimization** comes from context ...

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the auto-tuning approach to program **optimization** and performance portability to the
 2.1 Comparison of traditional **compiler** and auto-tuning capabilities 17 **DAG**.
 Directed Acyclic Graph. DDR. Double Data Rate (DRAM) ... **FMA**. Fused Multiply-Add.
 FMM. Fast Multipole Method ...

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Stage 2: Source Level **Optimization** and Verification. matching rules for the **compiler** and ii) the net-list for the functional units. 2.5 Code Generation ... Besides, it can deal with **DAG**. (Direct Acyclic Graph) for input DFGs and pattern graphs. tracting communication overhead and our **FMA** design re- ...

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Aug 31, 2010 ... SelectionDAG **Optimization** Phase: the **DAG** Combiner ... If a target supports floating point multiply-and-add (**FMA**) operations, The locals area is where the llvm **compiler** reserves space for local variables. ...
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Through **compilation** of specific require- ments for the medical domain, the 'GALEN Representation graph (**DAG**) where vertices are concept names (strictly speak- and extended to other species.8 **FMA** has four interrelated interest in the DL community to research on **optimization** ...

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linear transform can be transformed into an **FMA DAG** with all unfused multiplications collected at the outputs. Second, **Table VI** gives pseudocode for the **FMA** optimized**FMA optimization** performed by the **compiler**. For example, ...

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hole optimizer for utilizing fused multiply-add (**FMA**) instructions, and Section 5 ... **Table 2**.Instruction Count for Data Reordering Operations. constants within the **DAG**. Apart from commonplace **compiler optimizations** [16], ...

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using a set of rules to optimize the SIMD **DAG** obtained from the vectorizer. **Table 2**.Efficient Computation of $ecx := 17 * stride$ **FMA** extraction (but without backend**optimizations**) and (ii) Fftw codelets vector- ...

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is used to implement peephole **optimization** on the vector **DAG**. ... **Table 1**. Two-way

Vectorization. Two scalar add instructions are transformed into ... Spiral using the XL C

compiler's vectorizer and **FMA** extraction turned on, ...

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Mar 31, 2005 ... processor features, such as short-vector SIMD or **FMA**. ISA extensions, is imperative. Unfortunately, the methods used ... vectorizer is to completely cover the given scalar **DAG** by **Table 1** Relative costs of SIMD operations. facility of the XL C **compiler**. Figure 6. General **optimization** rule. ...

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compilers, optimization; G.4 [Mathematical Software]: Efficiency. General Terms JITed code for all of the programs in **Table 1** was 4625 bytes, which Our FP speculation algorithm runs on a direct acyclic graph (**DAG**) example, the IBM XL Fortran [10] **compiler** uses **FMA** by default, ...

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Example: The **FMA DAG** in Fig. 1(c) is the output of. Algorithm 1, when applied to the standard **DAG** in Second, **Table VI** gives pseudocode for the **FMA** optimized ... for each **pattern** of trivial entry locations in the propagated diagonals. Therefore, **FMA optimization** performed by the **compiler**. For example, ...
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the **table** to feed one input of a **full multiplier** (shown later in Fig- ure 2(a)). additions (a more precise name would be **addition/shift** chain; we **fusing** the optimal **DAGs** for **multiplying** by 45 (a) and 19 (b). Each ...

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